

Figure 1A
(Prior Art)

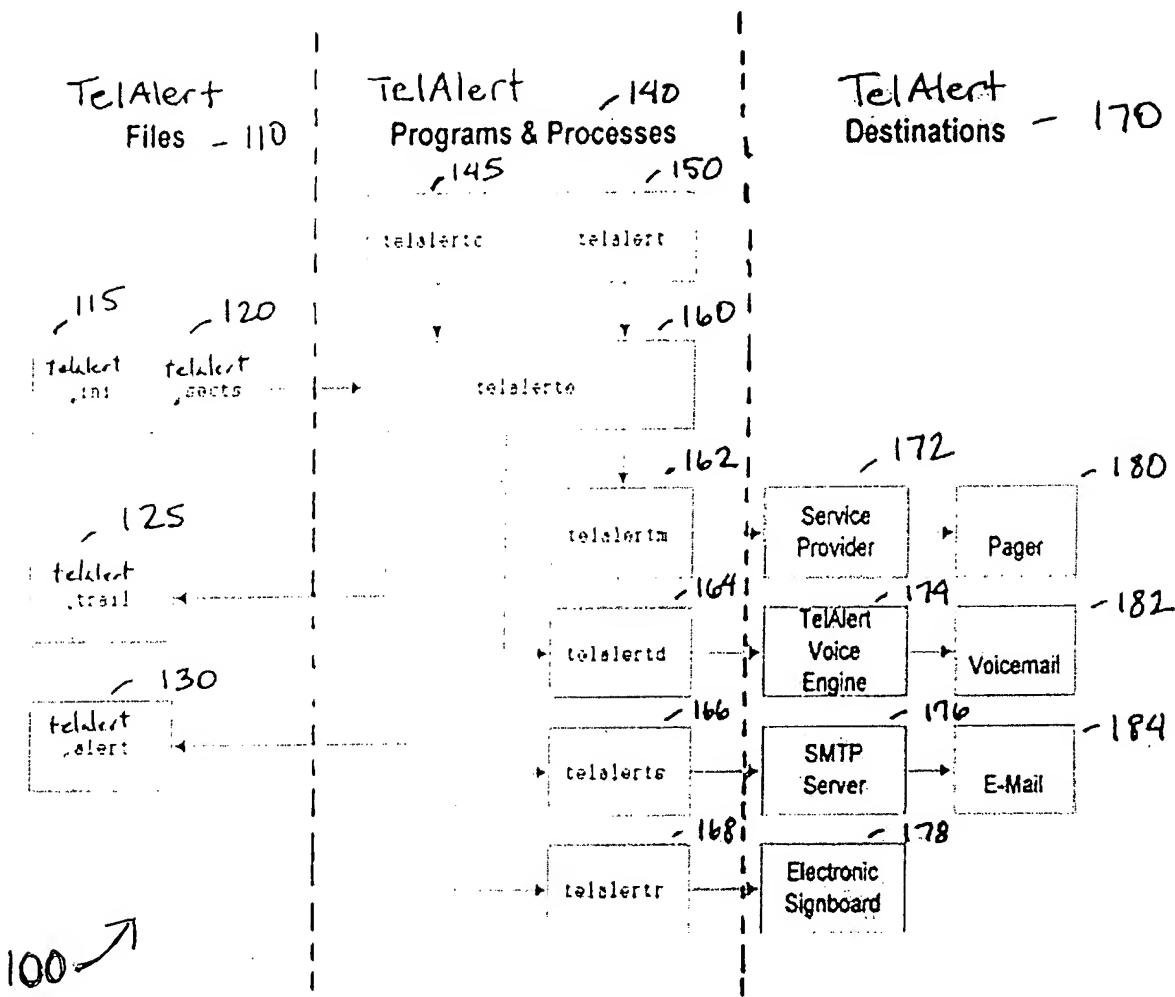


FIGURE 1B

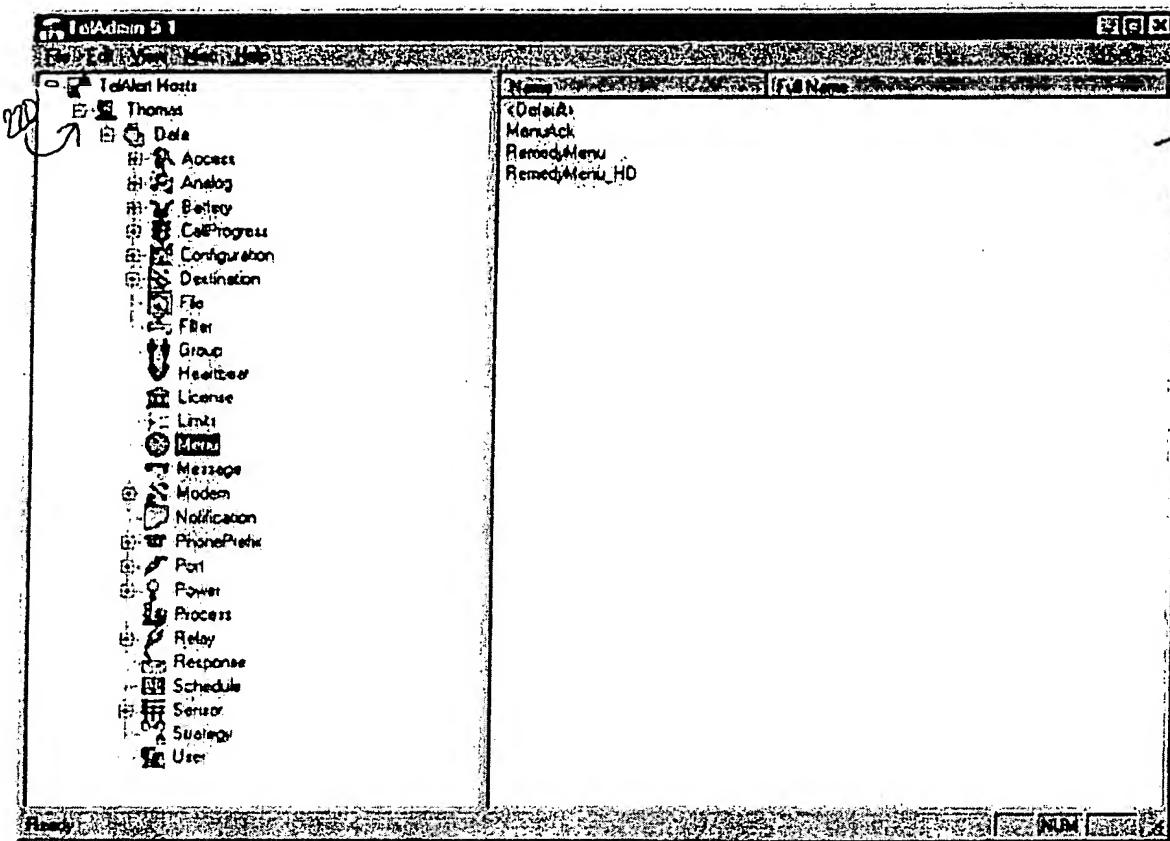


FIGURE 2 (PRIOR ART)

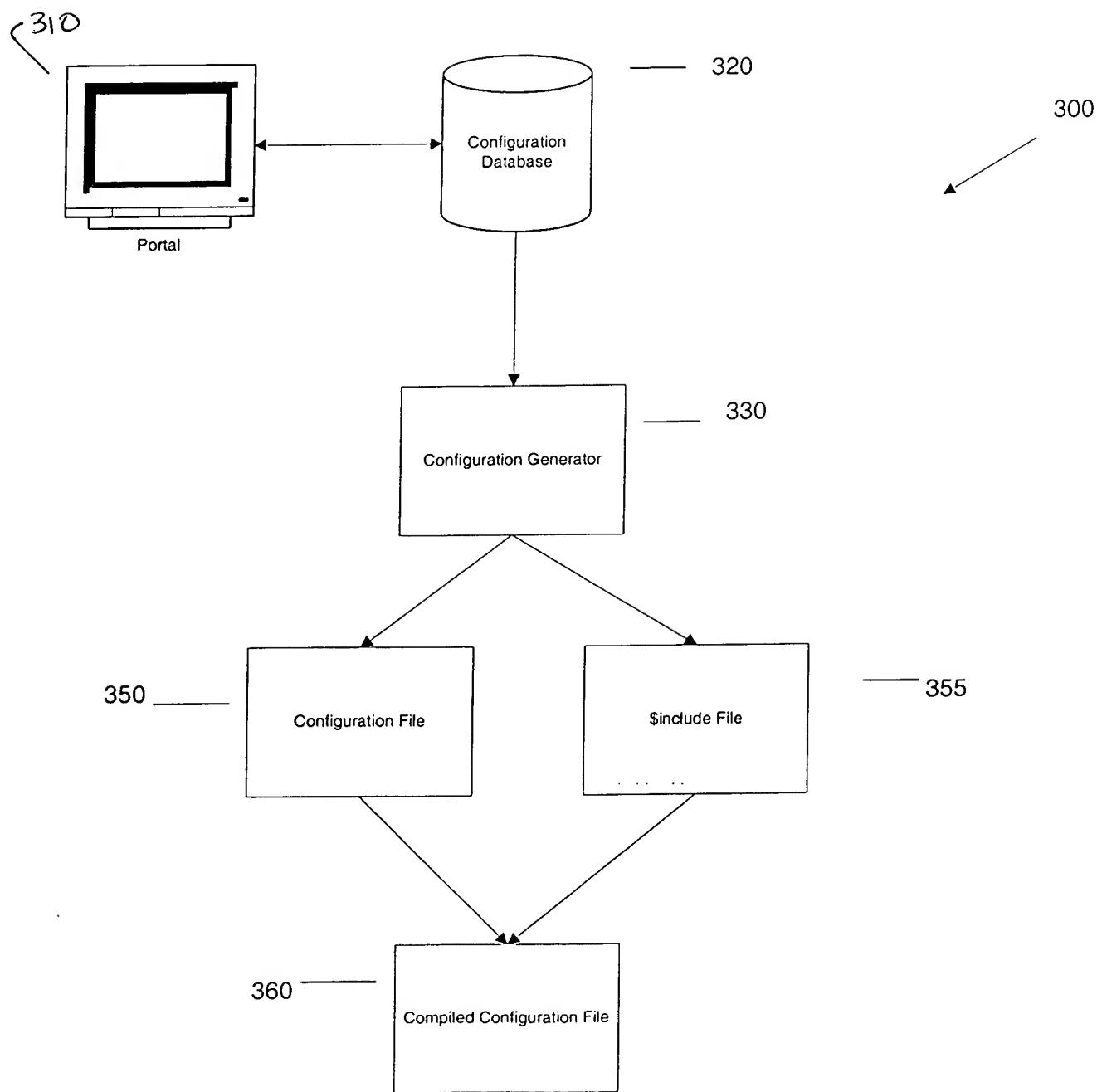


Figure 3

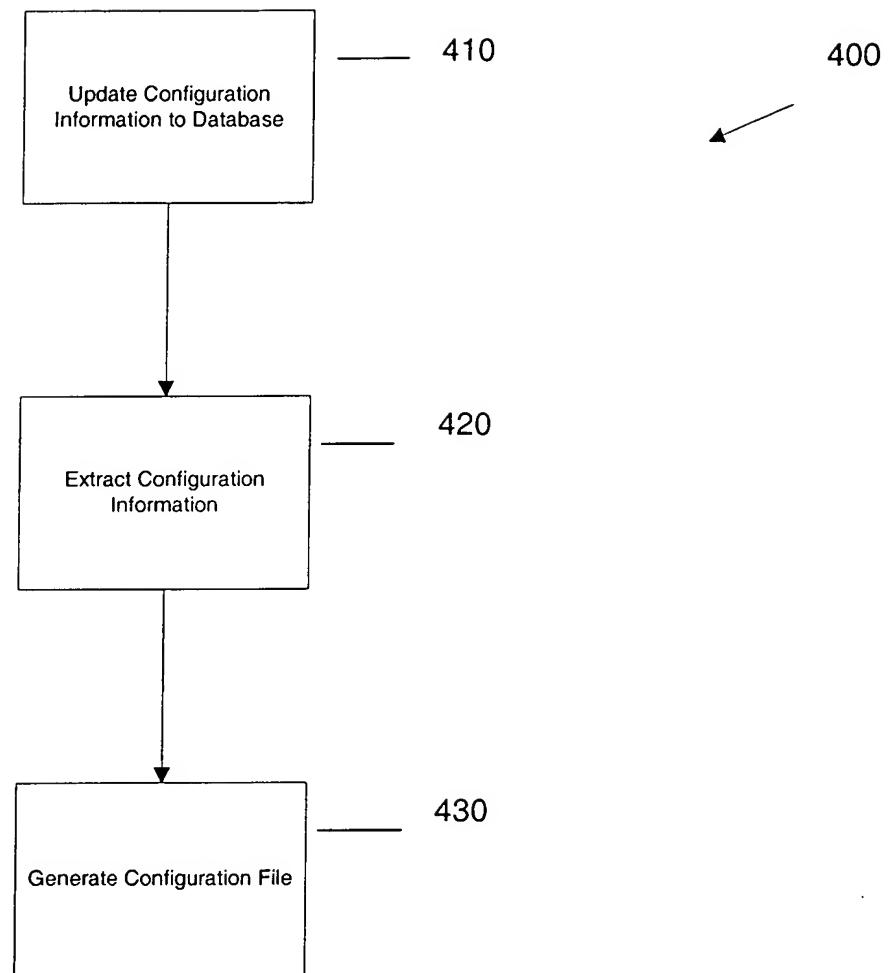
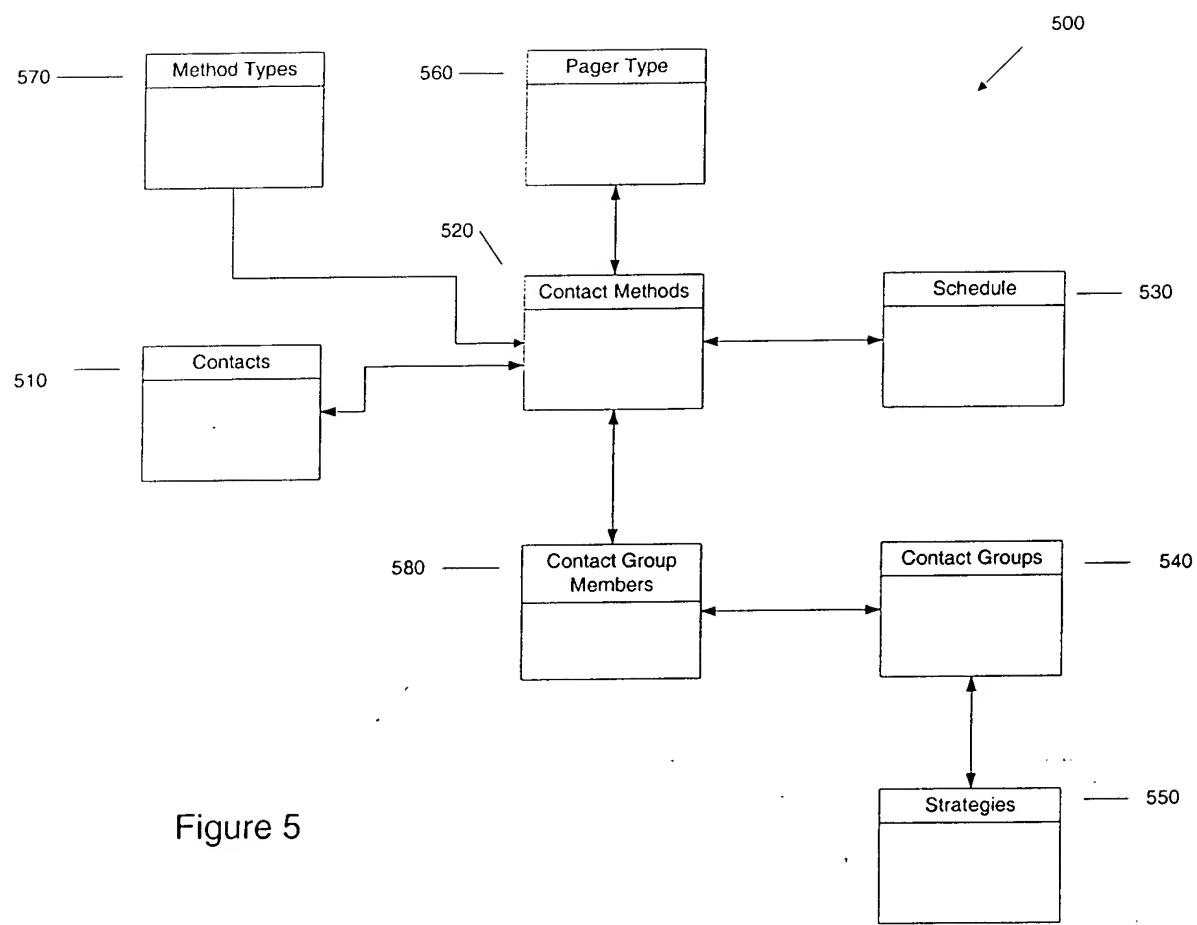


Figure 4



Digital Processing System

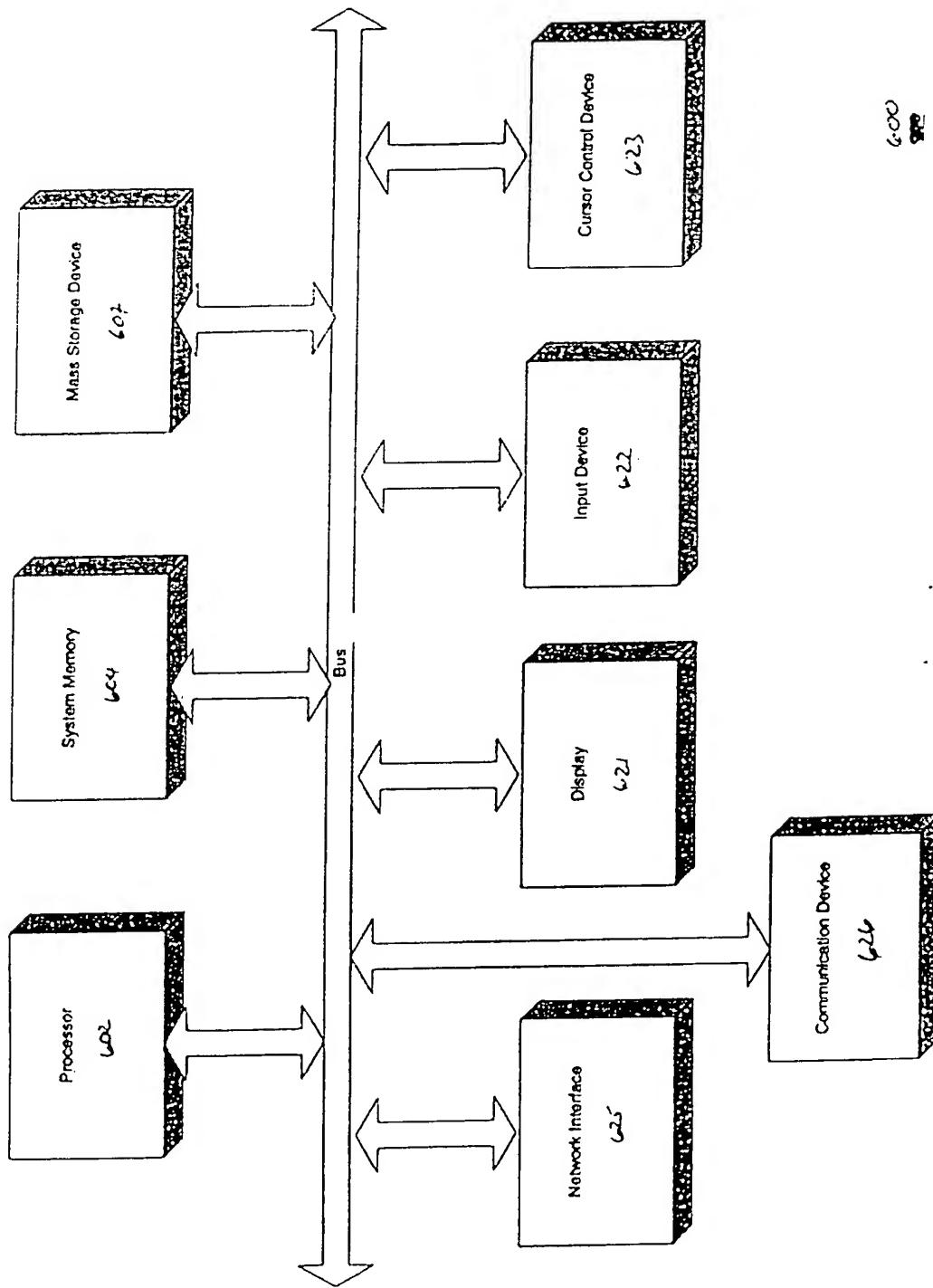


Figure 6